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Applicant(s)

Catherwood

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U.S. PATENT DOCUMENTS

DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
A. 3771146	11/6/73	Cottton et al.	710	260	1/22/73
B. 3781810	12/25/73	Downing	340	172.5	4/20/72
C. 3886524	5/27/75	Appelt	710	110	10/18/73
D. 4025771	5/24/77	Lynch et al.	708	521	3/25/74
E. 4074353	2/14/78	Woods et al.	710	264	3/24/76
F. 4090250	5/16/78	Carlson et al.	712	234	9/30/76
G. 4323981	4/6/82	Nakamura	364	749	10/23/78
H. 4379338	4/5/83	Nishitani et al.	708	552	11/21/80
I. 4398244	8/9/83	Chu et al.	364	200	5/7/80
J. 4408274	10/4/83	Wheatley et al	364	200	9/29/80
K. 4451885	5/29/84	Gerson et al.	708	200	3/1/82
L. 4473748	9/16/84	Yamazaki	364	900	8/27/81
M. 4481570	11/6/84	Bicknell	364	200	3/19/82
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Q.	0 554 917 A2	6/11/93	EP	G06F	9/26		X
R.	96/11443	4/16/96	WO	G06F	15/78	X	
S.	0 855 643 A1	07/29/98	EP	G06F	9/30	X	
T.	0 992 688	12/04/00	EP	G06F	9/32	X	
U.	0 992 889	12/14/00	EP	G06F	9/32	X	

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V.	Moon B I et al.: "A 32-bit RISC Microprocessor with DSP Functionality: Rapid Prototyping" IEICE Transactions on Fundamentals of Electronics, Communications and Computer Sciences, Institute of Electronics Information and Comm. Eng. Tokyo, JP, vol. E84-A no. 5, pages 1339-1347, XP001060025 ISSN: 0916-8508	5/2001
W.	Turley J: "Balancing Conflicting Requirements When Mixing RISC, BSPPs" Computer Design, Pennwell Publ. Littleton, Massachusetts, US, vol. 37, no. 10, pages 46, 48, 50-53, XP000860700 ISSN: 0010-4566	10/1998
X.	Levy M: "Microprocessor and DSP Technologies Unite for Embedded Applications" EDN Electrical Design News, Cahners Publishing Co., Newtown Massachusetts, US, no. Europe, pages 73-74, 76, 78-80, XP000779113 ISSN: 0012-7515	3724998

EXAMINER

Nro

DATE CONSIDERED

5/5/05

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.